



PATENT

Case Docket No. MICRON.116DV3

Date: October 26, 2004

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s)

Akram

Appl. No.

10/766,371

Filed

January 27, 2004

For

AIR SOCKET FOR TESTING

INTEGRATED CIRCUITS

Examiner

Ernest F. Karlsen

Group Art Unit:

2829

I hereby certify that this correspondence and all marked attachments are being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner for Patents, Mail Stop Amendment, P.O. Box 1450, Alexandria, VA 22313-1450, on

October 26, 2004

Brian Leubitz, Reg. No. 54,264

TRANSMITTAL LETTER

Mail Stop Amendment Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Dear Sir:

Enclosed for filing in the above-identified application are:

- (X) A Response to Restriction Requirement and Election of Species and Preliminary Amendment in 5 pages.
- (X) The Commissioner is hereby authorized to charge any additional fees which may be required, or credit any overpayment, to Account No. 11-1410.
- (X) Return prepaid postcard.

Brian Leubitz

Registration No. 54,264 Attorney of Record Customer No. 20,995

(415) 954-4114





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CERTIFICATE OF MAILING

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RESPONSE TO RESTRICTION REQUIREMENT AND ELECTION OF SPECIES AND PRELIMINARY AMENDMENT

Mail Stop Amendment

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Dear Sir:

In an action mailed October 5, 2004, the Examiner required restriction of prosecution to one of the following groups of claims:

Response to Restriction Requirement begins on page 2 of this paper.

Prior to examination on the merits in the above-referenced application, please amend the application as indicated below:

Amendments to the Claims are reflected in the listing of claims which begins on page 3 of this paper.

Remarks/Arguments begin on page 5 of this paper.

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RESPONSE TO RESTRICTION REQUIREMENT

In an action mailed October 5, 2004, the Examiner required restriction of prosecution to one of the following groups of claims:

Group I Claims 1-12, drawn to an electrical component testing device, classified in class 324, subclass 754;

Group II Claims 13-22, drawn to a device for testing a plurality of semiconductor devices, classified in class 324, subclass 754;

Group III Claims 23-28, drawn to a method of manufacturing a device for testing a plurality of electrical components, classified in class 29, subclass 825; and

Group IV Claims 29-32, drawn to a method of testing plurality of electrical components, classified in class 324, subclass 754.

In response, Applicants elect, without traverse, to proceed with examination on the merits of Group II (Claims 13-22), drawn to a device for testing a plurality of semiconductor devices, classified in class 324, subclass 754.